Notice of Allowability	Application No.	Applicant(s)
	10/605,235	YANG ET AL.
	Examiner	Art Unit
	ANH PHUNG	2824
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.31	6 (OR REMAINS) CLOSED in this ap or other appropriate communication BGHTS. This application is subject to	oplication. If not included not will be mailed in due course. THIS
1. This communication is responsive to		
2. The allowed claim(s) is/are 1-9.		
3. The drawings filed on 17 September 2003 are accepted by	y the Examiner.	
 4. Acknowledgment is made of a claim for foreign priority unerself. All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have 3. Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONITHIS THREE-MONTH PERIOD IS NOT EXTENDABLE 5. A SUBSTITUTE OATH OR DECLARATION must be subminsformal PATENT APPLICATION (PTO-152) which give 6. CORRECTED DRAWINGS (as "replacement sheets") must (a) including changes required by the Notice of Draftsper 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the central proper in the deposition of the priority documents have a copies of the priority docum	e been received. e been received in Application No comments have been received in this of this communication to file a reply MENT of this application. nitted. Note the attached EXAMINER es reason(s) why the oath or declara st be submitted. son's Patent Drawing Review (PTG 's Amendment / Comment or in the G 1.84(c)) should be written on the drawithe header according to 37 CFR 1.121(sit of BIOLOGICAL MATERIAL m	complying with the requirements C'S AMENDMENT or NOTICE OF ation is deficient. 948) attached Office action of ags in the front (not the back) of d).
attached Examiner's comment regarding REQUIREMENT		
	•	
Attachment(s) 1. ⋈ Notice of References Cited (PTO-892)	5 Motice of Informal P	atent Application (PTO-152)
2. Notice of Draftperson's Patent Drawing Review (PTO948)	6. ☐ Interview Summary	
	Paper No./Mail Dat	e .
 Information Disdosure Statements (PTO-1449 or PTO/SB/C Paper No./Mail Date 	08), 7. ⊠ Examiner's Amendr	nent/Comment
4. Examiner's Comment Regarding Requirement for Deposit	8. 🛛 Examiner's Stateme	ent of Reasons for Allowance
of Biological Material	9. ⊠ Other <u>Search Histor</u>	у.
	Ouch.	Pruny
	ANH PHU	MG AMINER

Application/Control Number: 10/605,235 Page 2

Art Unit: 2824

DETAILED ACTION

1. In response to the U.S. Application Ser. No. 10/605,235 filed on February 9, 2003, claims **1-9** are pending in the application.

Examiner's amendment

2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The specification has been amended as follows:

On paragraph [0001], after "Jan. 25, 2002", insert -- now U.S. Pat. No. 6,678,190, issued on Jan. 13, 2004, --

The examiner's amendment has been made in order to place the application in a condition for allowance.

Examiner's Statement of Reasons for Allowance

3. Claims **1-9** are allowed.

Keller et al. (US 6,128,232), Glasser (US 4,665,503) and Maeda (US 4,371,956) disclose an erasable programmable read only memory similar to that of the present application, but fail to teach:

"a first node of said first P-type MOS transistor is connected to source line voltage, a second node of said first P-type MOS transistor is connected to a first node of a second P-type MOS transistor, wherein a second node of said second P-type MOS transistor is connected to bit line voltage, wherein a gate of said second P-type MOS

Ą.

transistor serves as a floating gate, wherein said erasable programmable read only memory does not need to bias a certain voltage on a control gate for programming and thereby injecting hot carriers onto said floating gate, and wherein said erasable programmable read only memory is capped by dielectric materials which are transparent to ultraviolet (UV) light" as claimed in the independent claim 1; or

"a first node of said first P-type MOS transistor is connected to source line voltage, a second node of said first P-type MOS transistor is connected to a first node of a second P-type MOS transistor, wherein a second node of said second P-type MOS transistor is connected to bit line voltage, wherein a gate of said second P-type MOS transistor serves as a floating gate, wherein said erasable programmable read only memory is capped by dielectric materials which are transparent to ultraviolet light" as claimed in the independent claim **6**.

Any comments considered necessary by applicants must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comment on Statement of Reasons for Allowance".

Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **ANH PHUNG** whose telephone number is **(571) 272-1883**. The examiner can normally be reached on Monday-Friday from 8:00 AM to 4:30 PM.

Application/Control Number: 10/605,235 Page 4

Art Unit: 2824

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, RICHARD ELMS, can be reached on (571) 272-1869. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Art Unit: 2824

AKP

Out Plung